



US UTILITY Patent Application

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ART UNIT EXAMINER SUBCLASS CLASS APPLICATION NO. CONT/PRIOR 287435 2815 400 D 09/651385

**APPLICANTS** Sanjay Dabral Krishna Seshan

Diode and transistor design for high speed I/O

PTO-2040 12/99

ISSUING CLASSIFICATION							
ORIGINAL			CROSS REFERENCE(S)				
CLASS SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
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INTERNATIONAL CLASSIFICATION		*					
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TERMINAL	DRAWINGS			CLAIMS ALLOWED	
DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
				NOTICE OF ALLOWANCE MAILED	
The term of this patent subsequent to (date) has been disclaimed.	(Assistant	Examiner)	(Date)		
The term of this patent shall not extend beyond the expiration date of U.S Patent. No.				ISS	UE FEE
				Amount Due	Date Paid
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